UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/886,518	06/21/2001	Hosagrahar Somashekhar	YOTTA1250	6452

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GRAY, CARY, WARE & FREIDENRICH LLP
2000 University Avenue
E. Palo Alto, CA 94303-2248

EXAMINER
CHANG, RICHARD

ART UNIT	PAPER NUMBER
2663	

DATE MAILED: 12/27/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

PTO-90C (Rev. 10/03)

	Application No.	Applicant(s)
Office Action Summary	09/886,518	SOMASHEKHAR, HOSAGRAHAR
	Examiner	Art Unit	
	Richard Chang	2663	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>3</u> MONTH(S) FROM
THE MAILING DATE OF THIS COMMUNICATION.
- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed
  after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any
  earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *18 January 2002*.

2a)☐ This action is **FINAL**.   2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is
closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☐ Claim(s) _____ is/are pending in the application.

    4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1,7,9,11,12,20,21 and 25* is/are rejected.

7)☒ Claim(s) *2-6,8,10,13-19,22-24 and 26-31* is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

    Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

    Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

    a)☐ All   b)☐ Some * c)☐ None of:

      1.☐ Certified copies of the priority documents have been received.

      2.☐ Certified copies of the priority documents have been received in Application No. _____.

      3.☐ Copies of the certified copies of the priority documents have been received in this National Stage
application from the International Bureau (PCT Rule 17.2(a)).

    * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1)☒ Notice of References Cited (PTO-892)

2)☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)

3)☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
    Paper No(s)/Mail Date _____.

4)☐ Interview Summary (PTO-413)
    Paper No(s)/Mail Date. _____.

5)☐ Notice of Informal Patent Application (PTO-152)

6)☐ Other: _____.

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102

that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 7, 9 and 11-12 are rejected under 35 U.S.C. 102(e) as being clearly

anticipated by US patent No. 6, 434,115 ("Schwartz et al.").

Regarding claim 1, Schwartz et al. teach a systems and methods for switching

packets of digital data in a switching node used in a digital data network (a system for

transporting data from a plurality ingress to a plurality of egress line) (See Fig. 2, Col. 5,

lines 5-8) comprising of

an inter-port packet switch (22, data switching matrix), in the form of a crosspoint

switch, having a plurality of input ports coupled to of input port modules (20(n)) and a

plurality of output coupled to of output port modules (21(n)) wherein for each of the

ingress ports, the data switching matrix is configured to transport data from each the

ingress port to one of the plurality of egress ports (See Fig. 2, Col. 5, lines 48-50),

a number of input port modules (20(1) through 20(N), a plurality of ingress edge

units), each of which is coupled to one of the plurality of ingress ports of the an inter-

port packet switch (22, data switching matrix), wherein each of the plurality of ingress

edge units is configured to receive a data from a corresponding input ports (25(n)(1)

through 25(n)(M), one or more of a plurality of ingress lines) (See Fig. 2, Col. 5, lines

12-17),

a number of output port modules (21(1) through 21(N), a plurality of egress edge

units), each of which is coupled to one of the plurality of egress ports of the an inter-port

packet switch (22, data switching matrix), wherein each of the plurality of egress edge

units is configured to transmit data received from the data switching matrix to a plurality

of output (egress) ports (26(n)(1) through 26(n)(M), one or more of a plurality of egress

lines) (See Fig. 2, Col. 5, lines 24-29),

wherein, for each packet received, the input port module (20(n), each of the

plurality of ingress edge units) is configured to identifies from the destination address

contained in the each received packet's header (examine data received ... and to

identify portions of the data ... egress edge units),

wherein the packet (portions of the data) corresponding to each of the output port

module (21(n), egress edge units) is stored in a corresponding buffer and wherein data

in each buffer is transmitted to the corresponding egress edge unit (21(n)) via the inter-

port packet switch (22, data switching matrix) corresponding to real time meta-data

packet control signals (in a predetermined time slots) (See Fig. 2, Col. 5, lines 21-31).

Regarding claim 11, Schwartz et al. further teach that there is a plurality of e

buffers of the packets received from the network interface (30) in the packet memory

(31) for all input (ingress) ports 25(n) in the input port module 20(n), and the control

circuit (33) may also receives packet transfer requests directly via a packet segment

generator (34) to controls retrieval and transfer of packets from the packet memory (31)

to the inter-port packet switch (22) for transfer to the output port modules 21(n)

(independent of the predetermined time slot) (See Fig. 2, Col. 10, lines 30-35).

Regarding claims 7 and 12, Schwartz et al. further teach that the communication

links 13(p) may utilize any convenient information transmission medium including optical

fiber links for carrying optical signals (See Fig. 1, Col. 5, lines 18-21).

Regarding claim 9, Schwartz et al. further teach that the input port module 20(n)

has a plurality of input (ingress) ports (25(n)(1) through 25(n)(M)) which are connected

to respective one of the communication links 13(p) for receiving packets (component

with line function) (See Fig. 2, Col. 5, lines 15-20) and function modules to transfer the

packet to the switching fabric (component with service function) (See Fig. 2, Col. 5, lines

32-35).

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102

that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on
sale in this country, more than one year prior to the date of application for patent in the United States.

4. Claims 20-21 and 25 are rejected under 35 U.S.C. 102(b) as being clearly

anticipated by US patent No. 5,126,999 ("Munter et al.").

Regarding claims 20 and 21, Munter et al. teach a method for high speed packet

switching and to space-division switching by means of input-buffered NxN switching or

cross-point matrices (a method for transporting data) (See Fig. 1 and Fig. 3, Col. 5, lines

25-27) comprising of

demultiplexing (parsing) each incoming data stream (received data stream)

header in header decoder (41) into the appropriate packet (a plurality of data cells) (See

Fig. 4, Col. 5, lines 58-60),

decoding (identifying) the packet (plurality of data cells) header in header

decoder (41) for packet's switch output port destination (See Fig. 4, Col. 5, lines 63-66),

storing (segregating) the packets (plurality of data cells) into the plural FIFOs (F1

to F16, distinct sets of data cells)

based on the packet's switch output port destination (wherein the data set of a

plurality of data cells has a common destination)) (See Fig. 4, Col. 5, lines 58-

63), and

operating dynamically on a real-time basis (sequentially transmitting) by means

of an N crosspoint selector (33) to yield a new selection of N crosspoints in the switch

(30) for connection of the input packets and buffers (distinct sets of a plurality of data

cells) to the corresponding destinations (See Fig. 3, Col. 5, lines 30-40).

Regarding claim 25, Munter et al. further teach that storing (segregating) the

packets (plurality of data cells) into plural FIFOs (F1 to F16, distinct sets of data cells)

having the same packet's switch output port destination (wherein the data set of a

plurality of data cells has a common destination)) (See Fig. 4, Col. 5, lines 58-63).

Allowable Subject Matter

5. *Claims 2-6,8,10,13-19,22-24 and 26-31* are objected to as being dependent upon a

rejected base claim, but would be allowable if rewritten in independent form including all

of the limitations of the base claim and any intervening claims and if no art rejection can

be applied.

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Richard Chang whose telephone number is (571) 272-

3129. The examiner can normally be reached on Monday - Friday from 8 AM to 5 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Chau T Nguyen can be reached on (571) 272-3126. The fax phone number for

the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

Richard Chang
Patent Examiner
Art Unit 2663

rkc

CHAU NGUYEN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600